

06-10-08

47/1765 *HW*



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By: *Patricia M.*

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Attorney Docket No.: 100806-00199 (NEKA 18.612)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Yoshihisa MATSUBARA et al.

Serial No.: 09/840,386 ✓

Filed: April 23, 2001

Title: **METHOD FOR MANUFACTURING SEMICONDUCTOR
DEVICE, AND PROCESSING SYSTEM AND
SEMICONDUCTOR DEVICE**

Examiner: Lan Vinh

Group Art Unit: 1765

Confirmation No.: 2510

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AMENDMENT

SIR:

In response to the Office Action issued March 10, 2005, the period for response being set to expire on June 10, 2005, please amend the above-captioned application as follows.

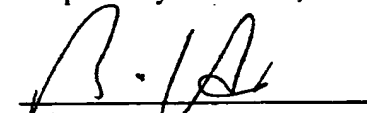
alone. New claim 18 recites that the wire connects to the p-region, and therefore claim 18 is allowable over the cited references.

Conclusion

In view of the above remarks, it is believed that claims 1, 2, and 13-18 are in condition for allowance, which action is respectfully solicited. However, if for any reason the Examiner should consider this application not to be in condition for allowance, the Examiner is respectfully requested to telephone the undersigned attorney at the number listed below prior to issuing a further Action.

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Respectfully submitted,



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